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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT
Yoshimoto TAMURAFILING DATE
March 3, 2004

10/790,826

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
①	1	6,064,580	05/16/2000	WATANABE et al.		

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
①	2	JP A 11-262263 w/ abst & trans	09/24/1999	Japan		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

⑧	3	Watanabe, Haruo et al., "BHB (Boost Half Bridge) Power Supply," <i>The Institute of Electronics, Information and Communication Engineers</i> , Technical Report of IEICE, EE98-17, (1998-07), pp. 27-34				
⑧	4	Watanabe, Haruo, "High Efficiency On-board Power Supply using Boost Half Bridge Method," <i>Switching Power Supply Symposium 2000</i> , Japan Management Association, B2-2				
①	5	Matsuda, Yoshiaki, "Low Voltage Output High Efficiency DC/DC Converter," <i>Switching Power Supply Symposium 2001</i> , Japan Management Association, A2-3				

EXAMINER

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DATE CONSIDERED

May 05

Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: March 3, 2004